

Notice of References Cited		Application/Control No. 09/978,358	Applicant(s)/Patent Under Reexamination BROCK ET AL.	
		Examiner Mark Connolly	Art Unit 2115	Page 1 of 1

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*	B	US-6,566,918	05-2003	Nguyen, Andy T.	327/115
*	C	US-6,639,441	10-2003	Ono et al.	327/175
*	D	US-6,781,419	08-2004	Harrison, Ronnie M.	326/95
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NON-PATENT DOCUMENTS

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	U	Nelson Victor P. et al., Digital Logic Circuit Analysis and Design, 1995, Prentice-Hall Inc., pages 268-277
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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